

Amendments to the Specification and/or Abstract:

Please amend the specification as follows:

Replace the paragraph on page 2, between lines 18-24 of the specification with the following:

The invention provides for a data processing apparatus according to Claim 1. This data processing apparatus is of a type, such as a VLIW processor, that uses a central control of program flow with an instruction address that addresses an instruction word that contains a plurality of instructions for different functional units. However, during program execution the translation of the instruction address into physical addresses can be modified selectively for a particular one of the functional units or a group of the functional units individually.

Replace the paragraph on page 7, between lines 8-17 of the specification with the following:

Although it has been assumed in this explanation that controller 14 supplies the same addresses to all memory units 12a-c, this is not in fact necessary. Without deviating from the invention controller 14 may apply different forms of mapping. Similarly, although local branch functional unit 18e has been shown in the group of functional units 90a-e-70a-c whose instruction address is modified, it should be understood that without deviating from the invention local branch functional unit 18e may be located in any group of functional units 70c. Furthermore, although only one local branch functional unit 18e has been shown for one memory unit 12c it should be understood that more than one local branch unit may be provided for modifying the offset for the same memory unit 12c, e.g. for modifying the offset in offset register 15b or in a plurality of offset registers.